

Publications

Hardware/Software Co-Design

- [Heidorn C.](#), Hannig F., Riedelbauch D., Strohmeyer C., Teich J.:
[Entropy Sampling-Based Neural Architecture Search for Resource-Constrained Microcontroller Targets](#)
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- [Heidorn C.](#), Hannig F., Riedelbauch D., Strohmeyer C., Teich J.:
[OpTC - Automatic Compression and Performance Estimation for Deployment of Neural Networks on AURIX TC3xx Microcontrollers](#)
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